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[2885/84]



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s) : Martin VORBACH et al.
Serial No. : 10/792,168
Filing Date : March 2, 2004
For : I/O AND MEMORY BUS SYSTEM FOR DFPS AS WELL
AS MODULES HAVING TWO- OR MULTIDIMENSIONAL
PROGRAMMABLE CELL STRUCTURES
Group Art Unit : 2111
Examiner : Gopal C. Ray
Confirmation No. : 3713

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Feb 22 2007

Michelle M. Carniaux (Reg. No. 36,098)

NOTICE OF APPEAL

SIR:

Applicants hereby appeal to the Board of Patent Appeals and Interferences
from the decision of the Examiner made in the Final Office Action dated August 18, 2006.

The Commissioner is hereby authorized to charge payment of the 37 C.F.R.
§ 41.20(b)(1) Notice of Appeal fee of \$250.00 (small entity fee). The Commissioner is
authorized to charge any additional fees or credit any overpayment in connection with this
paper to the deposit account of **Kenyon & Kenyon LLP**, Deposit Account No. **11-0600**. A
duplicate of this letter is enclosed.

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Dated: Feb. 2, 2007

Respectfully submitted,

KENYON & KENYON LLP

By:

Michelle Carniaux
Reg. No. 36,098
One Broadway
New York, NY 10004
(212) 425-7200

CUSTOMER NO. 26646